



PATENT

Date of Notice
of Allowance : April 4, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Signature

Confirmation No. 9113

Applicant : Janardhanan S. Ajit
Application No. : 10/621,005
Filed : July 16, 2003
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT
OUTPUT (I/O) CIRCUIT

Grp./Div. : 2816
Examiner : Tuan Thieu Lam

Docket No. : 50716/RJP/B600

AMENDMENT UNDER 37 CFR § 1.312

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
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Post Office Box 7068
Pasadena, CA 91109-7068
June 20, 2005

Commissioner:

Please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.